



# CQE50W Series Application Note V14 April 2019

## ISOLATED DC-DC CONVERTER CQE50W SERIES APPLICATION NOTE



### Approved By:

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# CQE50W Series

## Application Note V14 April 2019

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### Contents

1. Introduction .....	3
2. DC-DC Converter Features .....	3
3. Electrical Block Diagram .....	3
4. Technical Specifications .....	4
5. Main Features and Functions .....	8
5.1 Operating Temperature Range .....	8
5.2 Output Voltage Adjustment .....	8
5.3 Over Current Protection .....	8
5.4 Output Overvoltage Protection .....	8
5.6 Remote On/Off .....	8
5.7 UVLO (Under Voltage Lock Out) .....	8
5.8 Over Temperature Protection .....	8
6. Applications .....	8
6.1 Recommended Layout, PCB Footprint and Soldering Information .....	8
6.2 Convection Requirements for Cooling .....	9
6.3 Thermal Considerations .....	9
6.4 Power Derating .....	10
6.5 Quarter Brick Heat Sinks: .....	12
6.6 Efficiency VS. Load: .....	13
6.7 Test Set-Up .....	15
6.8 Output Voltage Adjustment .....	15
6.9 Output Remote Sensing .....	16
6.10 Output Ripple and Noise .....	16
6.11 Output Capacitance .....	16
7. Safety & EMC .....	17
7.1 Input Fusing and Safety Considerations .....	17
7.2 EMC Considerations .....	17
8. Part Number .....	23
9. Mechanical Specifications .....	23
9.1 Mechanical Outline Diagrams .....	23



# CQE50W Series

## Application Note V14 April 2019

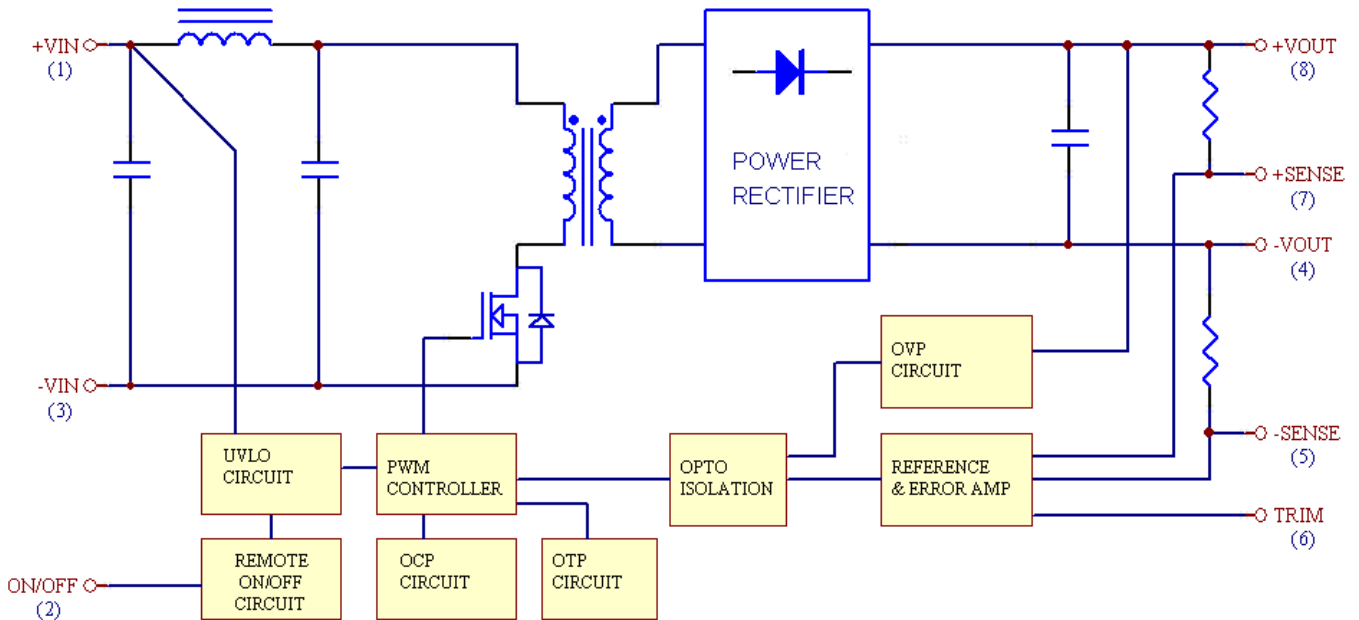
### 1. Introduction

This specification describes the features and functions of Cincon's CQE50W series of isolated DC-DC Converters. These are highly efficient, reliable and compact, high power density, single output DC/DC converters. The modules can be used in the field of telecommunications, data communications, wireless communications, servers etc. The CQE50W series can deliver up to 10A output current and provide a precisely regulated output voltage over a wide range of input voltages ( $V_i = 9- 36$  or  $18- 75V_{dc}$ ). The modules can achieve high efficiency up to 92%. The module offers direct cooling of dissipative components for excellent thermal performance. Standard features include remote On/Off, remote sense, output voltage adjustment, over voltage, over current and over temperature protection. The CQE50W series also have the following options: remote On/Off (positive or negative).

### 2. DC-DC Converter Features

- 50W Isolated Output
- No Tantalum Capacitor inside
- Quarter-Brick Size, Six-Sided Shield Metal Case
- High Efficiency up to 92%
- 300K Hz Switching Frequency
- 4:1 Input Range
- Regulated Output
- Input Under Voltage Lockout Protection
- Over Current Protection
- Remote On/Off
- Continuous Short Circuit Protection
- Full Load Operation up to 80°C  
with Heat-sink QBT210 (M-C421) Natural Convention
- UL60950-1 2<sup>nd</sup> Approval

### 3. Electrical Block Diagram



Electrical Block Diagram for other modules



# CQE50W Series

## Application Note V14 April 2019

### 4. Technical Specifications

(All specifications are typical at nominal input, full load at 25°C unless otherwise noted.)

#### ABSOLUTE MAXIMUM RATINGS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Input Voltage						
Continuous		24SXX	-0.7		36	V <sub>dc</sub>
		48SXX	-0.7		75	
Transient	100ms	24SXX			50	V <sub>dc</sub>
		48SXX			100	
Operating Case Temperature		All	-40		105	°C
Storage Temperature		All	-55		125	°C
Isolation Voltage	1 minute; input/output, input/case, output/case	All	1500			V <sub>dc</sub>

#### INPUT CHARACTERISTICS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Operating Input Voltage		24SXX	9	24	36	V <sub>dc</sub>
		48SXX	18	48	75	
Input Undervoltage Lockout						
Turn-On Voltage Threshold		24SXX	8	8.5	8.8	V <sub>dc</sub>
		48SXX	16.5	17	17.5	
Turn-Off Voltage Threshold		24SXX	7.7	8	8.3	V <sub>dc</sub>
		48SXX	15.5	16	16.5	
Lockout Hysteresis Voltage		24SXX		0.8		V <sub>dc</sub>
		48SXX		1		
Maximum Input Current	100% Load, V <sub>in</sub> =9V for 24SXX	24SXX		6.5		A
	100% Load, V <sub>in</sub> =18V for 48SXX	48SXX		3.2		
No-Load Input Current		24S3V3		100		mA
		24S05		100		
		24S12		100		
		24S15		100		
		24S24		60		
		24S48		60		
		48S3V3		60		
		48S05		60		
		48S12		60		
		48S15		60		
		48S24		60		
		48S48		60		
Inrush Current (I <sup>2</sup> t)		All			0.1	A <sup>2</sup> s
Input Reflected Ripple Current	P-P thru 12uH inductor, 5Hz to 20MHz	All		30		mA



## CQE50W Series Application Note V14 April 2019

### OUTPUT CHARACTERISTICS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Output Voltage Set Point	$V_{in}$ =Nominal $V_{in}$ , $I_o = I_{o\_max}$ , $T_c=25^\circ\text{C}$	$V_o=3.3\text{ V}$	3.267	3.3	3.333	$V_{dc}$
		$V_o=5.0\text{ V}$	4.95	5	5.05	
		$V_o=12\text{ V}$	11.88	12	12.12	
		$V_o=15\text{ V}$	14.85	15	15.15	
		$V_o=24\text{ V}$	23.76	24	24.24	
$V_o=48\text{ V}$	47.52	48	48.48			
Output Voltage Regulation						
Load Regulation	$I_o=I_{o\_min}$ to $I_{o\_max}$	All			$\pm 0.2$	%
Line Regulation	$V_{in}$ =low line to high line	All			$\pm 0.2$	%
Temperature Coefficient	$T_c=-40^\circ\text{C}$ to $105^\circ\text{C}$	All			$\pm 0.03$	%/ $^\circ\text{C}$
Output Voltage Ripple and Noise	5Hz to 20MHz bandwidth					
Peak-to-Peak	Full load 10uF tantalum and 1.0uF ceramic capacitors (for $V_o:48\text{V}$ : Full Load 10uF aluminum and 1uF ceramic)	$V_o=3.3\text{V}\&5.0\text{V}$			100	mV
		$V_o=12\text{V}\&15\text{V}$			150	
		$V_o=24\text{V}$			240	
		$V_o=48\text{V}$			480	
RMS	Full load, 10uF tantalum and 1.0uF ceramic capacitors (for $V_o:48\text{V}$ : Full Load 10uF aluminum and 1uF ceramic)	$V_o=3.3\text{V}\&5.0\text{V}$			40	mV
		$V_o=12\text{V}\&15\text{V}$			60	
		$V_o=24\text{V}$			100	
		$V_o=48\text{V}$			200	
Operating Output Current Range		$V_o=3.3\text{ V}$	0		10	A
		$V_o=5.0\text{ V}$	0		10	
		$V_o=12\text{ V}$	0		4.16	
		$V_o=15\text{ V}$	0		3.33	
		$V_o=24\text{ V}$	0		2.08	
$V_o=48\text{ V}$	0		1.04			
Output DC Current Limit Inception	Output Voltage=90% Nominal Output Voltage	All	110	125	165	%
Maximum Output Capacitance	Full load (resistive)	$V_o=3.3\text{ V}$			10000	$\mu\text{F}$
		$V_o=5.0\text{ V}$			10000	
		$V_o=12\text{ V}$			4160	
		$V_o=15\text{ V}$			3330	
		$V_o=24\text{ V}$			2080	
$V_o=48\text{ V}$	10		1040			

### DYNAMIC CHARACTERISTICS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Output Voltage Current Transient						
Step Change in Output Current	75% to 100% of $I_{o\_max}$	All			$\pm 5$	%
Setting Time (within 1% $V_{out}$ nominal)	$d_i/d_t=0.1\text{A/us}$	All			500	us



## CQE50W Series Application Note V14 April 2019

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Turn-On Delay and Rise Time						
Turn-On Delay Time, From On/Off Control	$V_{on/off}$ to $10\%V_{o\_set}$	All		10		ms
Turn-On Delay Time, From Input	$V_{in\_min}$ to $10\%V_{o\_set}$	All		10		ms
Output Voltage Rise Time	$10\%V_{o\_set}$ to $90\%V_{o\_set}$	All		10		ms

### EFFICIENCY

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units	
100% Load	$V_{in} = 12V_{dc}$	24S3V3		90		%	
		24S05		91			
		24S12		91			
		24S15		91.5			
		24S24		90			
		24S48		88.5			
	$V_{in} = 24V_{dc}$	48S3V3		90			
		48S05		91.5			
		48S12		92			
		48S15		91			
		48S24		91			
		48S48		89			
	$V_{in} = 24V_{dc}$	24S3V3			90.5		
		24S05			91.5		
		24S12			91.5		
		24S15			91.5		
		24S24			90		
		24S48			88.5		
	$V_{in} = 48V_{dc}$	48S3V3			90		
		48S05			92		
48S12				92			
48S15				91			
48S24				90.5			
48S48				89			

### ISOLATION CHARACTERISTICS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Isolation Voltage	1 minute; input/output, input/case, output/case	All			1500	$V_{dc}$
Isolation Resistance		All	10			M $\Omega$
Isolation Capacitance	Input/Output	All		1500		pF
	Input/Case					
	Output/Case			1000		



## CQE50W Series Application Note V14 April 2019

### FEATURE CHARACTERISTICS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Switching Frequency		All		300		KHz
On/Off Control, Positive Remote On/Off logic						
Logic Low (Module Off)	$V_{on/off}$	All	0		1.2	V
Logic High (Module On)	$V_{on/off}$	All	3.5 or Open Circuit		75	V
On/Off Control, Negative Remote On/Off logic						
Logic High (Module Off)	$V_{on/off}$	All	3.5 or Open Circuit		75	V
Logic Low (Module On)	$V_{on/off}$	All	0		1.2	V
ON/OFF Current (for both remote on/off logic)	$I_{on/off}$ at $V_{on/off}=0.0V$	All		0.3	1	mA
Leakage Current (for both remote on/off logic)	Logic High, $V_{on/off}=15V$	All			30	uA
Off Converter Input Current	Shutdown input idle current	All		4	10	mA
Output Voltage Trim Range	$P_{out}$ =max rated power	All	-10		+10	%
Output Over Voltage Protection		All	115	125	140	%
Over-Temperature Shutdown		All		110		°C

### GENERAL SPECIFICATIONS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
MTBF	$I_o=100\%$ of $I_{o,max}$ ; $T_a=25^\circ C$ per MIL-HDBK-217F	XXS24		800		K hours
		XXS48				
		Others		600		
Weight		All		63		grams



# CQE50W Series

## Application Note V14 April 2019

### 5. Main Features and Functions

#### 5.1 Operating Temperature Range

The CQE50W series converters can be operated within a wide case temperature range of  $-40^{\circ}\text{C}$  to  $105^{\circ}\text{C}$ . Consideration must be given to the derating curves when ascertaining maximum power that can be drawn from the converter. The maximum power drawn from quarter brick models is influenced by usual factors, such as:

- Input voltage range
- Output load current
- Forced air or natural convection

#### 5.2 Output Voltage Adjustment

Section 6.8 describes in detail how to trim the output voltage with respect to its set point. The output voltage on all models is adjustable within the range of  $+10\%$  to  $-10\%$ .

#### 5.3 Over Current Protection

The converter is protected against over current or short circuit conditions. At the instance of current-limit inception, the module enters a hiccup mode of operation, whereby it shuts down and automatically attempts to restart. While the fault condition exists, the module will remain in this hiccup mode, and can remain in this mode until the fault is cleared. The unit operates normally once the output current is reduced back into its specified range.

#### 5.4 Output Overvoltage Protection

The output overvoltage protection consists of circuitry that internally limits the output voltage. If more accurate output over voltage protection is required then an external circuit can be used via the remote on/off pin.

#### 5.6 Remote On/Off

The CQE50W series allows the user to switch the module on and off electronically with the remote on/off feature. All models are available in "positive logic" and "negative logic" (optional) versions. The converter turns on if the remote on/off pin is high ( $>3.5\text{Vdc}$  or open circuit). Setting the pin low (0 to  $<1.2\text{Vdc}$ ) will turn the converter off. The signal level of the remote on/off input is defined with respect to ground. If not using the remote on/off pin, leave the pin open (converter will be on). Models with part number suffix "N" are the "negative logic" remote On/Off version. The unit turns off if the remote on/off pin is high ( $>3.5\text{Vdc}$  or open circuit). The converter turns on if the on/off pin input is low (0 to  $<1.2\text{Vdc}$ ). Note that the converter is off by default.

#### 5.7 UVLO (Under Voltage Lock Out)

Input under voltage lockout is standard on the CQE50W unit. The unit will shut down when the input voltage drops below a threshold, and the unit will operate when the input voltage goes above the upper threshold.

#### 5.8 Over Temperature Protection

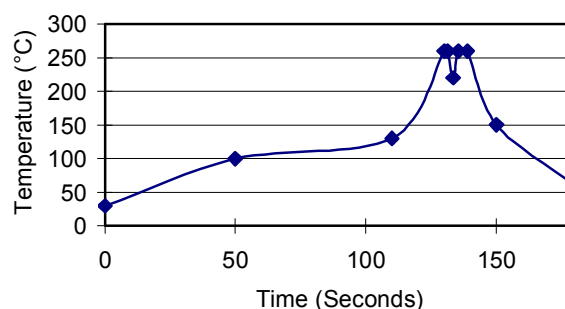
These modules have an overtemperature protection circuit to safeguard against thermal damage. Shutdown occurs with the maximum case reference temperature is exceeded. The module will restart when the case temperature falls below over temperature shutdown threshold.

### 6. Applications

#### 6.1 Recommended Layout, PCB Footprint and Soldering Information

The system designer or end user must ensure that metal and other components in the vicinity of the converter meet the spacing requirements for which the system is approved. Low resistance and inductance PCB layout traces are the norm and should be used where possible. Due consideration must also be given to proper low impedance tracks between power module, input and output grounds. The recommended soldering profile and PCB layout are shown below.

Lead Free Wave Soldering Profile



Note :

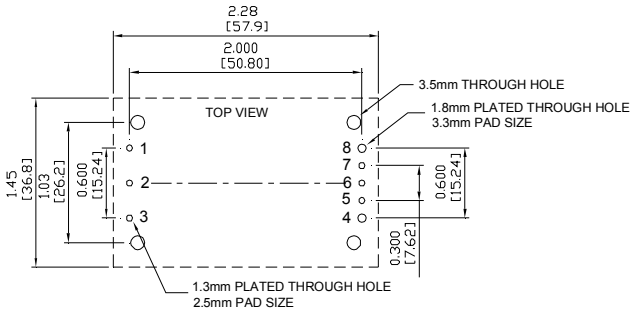
1. Soldering Materials: Sn/Cu/Ni
2. Ramp up rate during preheat:  $1.4^{\circ}\text{C}/\text{Sec}$  (From  $50^{\circ}\text{C}$  to  $100^{\circ}\text{C}$ )
3. Soaking temperature:  $0.5^{\circ}\text{C}/\text{Sec}$  (From  $100^{\circ}\text{C}$  to  $130^{\circ}\text{C}$ ),  $60\pm 20$  seconds
4. Peak temperature:  $260^{\circ}\text{C}$ , above  $250^{\circ}\text{C}$  3~6 Seconds
5. Ramp up rate during cooling:  $-10.0^{\circ}\text{C}/\text{Sec}$  (From  $260^{\circ}\text{C}$  to  $150^{\circ}\text{C}$ )





# CQE50W Series

## Application Note V14 April 2019



### 6.2 Convection Requirements for Cooling

To predict the approximate cooling needed for the Quarter brick module, refer to the power derating curves in section 6.4. These derating curves are approximations of the ambient temperatures and airflows required to keep the power module temperature below its maximum rating. Once the module is assembled in the actual system, the

module's temperature should be monitored to ensure it does not exceed 105°C as measured at the center of the top of the case (thus verifying proper cooling).

### 6.3 Thermal Considerations

The power module operates in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat is removed by conduction, convection, and radiation to the surrounding environment. The power output of the module should not be allowed to exceed rated power ( $V_{o\_set} \times I_{o\_max}$ ).

The power modules have through-threaded, M3 x0.5 mounting holes, which enable heat sinks or cold plates to be attached to the module. Thermal de-rating with heat sinks is expressed by using the overall thermal resistance of the module ( $R_{ca}$ ).

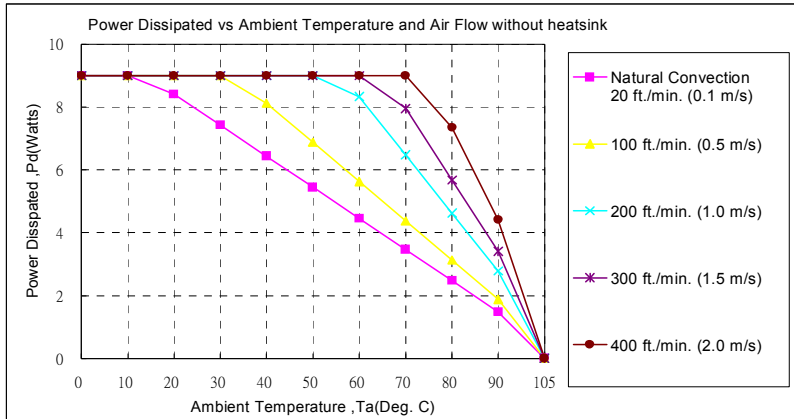


# CQE50W Series Application Note V14 April 2019

## 6.4 Power Derating

The operating case temperature range of CQE50W series is  $-40^{\circ}\text{C}$  to  $+105^{\circ}\text{C}$ . When operating the CQE50W series, proper de-rating or cooling is needed. The maximum case temperature under any operating condition should not exceed  $105^{\circ}\text{C}$ .

### Forced Convection Power De-rating without Heat Sink



AIR FLOW RATE	TYPICAL $R_{ca}$
Natural Convection 20ft./min. (0.1m/s)	10.1 $^{\circ}\text{C}/\text{W}$
100 ft./min. (0.5m/s)	8.0 $^{\circ}\text{C}/\text{W}$
200 ft./min. (1.0m/s)	5.4 $^{\circ}\text{C}/\text{W}$
300 ft./min. (1.5m/s)	4.4 $^{\circ}\text{C}/\text{W}$
400 ft./min. (2.0m/s)	3.4 $^{\circ}\text{C}/\text{W}$

Example (without heatsink):

What is the minimum airflow necessary for a CQE50W-48S05 operating at nominal line voltage, an output current of 10A, and a maximum ambient temperature of  $60^{\circ}\text{C}$ ?

Solution:

Given:  $V_{in}=48V_{dc}$ ,  $V_o=5V_{dc}$ ,  $I_o=10A$

Determine Power dissipation ( $P_d$ ):

$$P_d = P_i - P_o = P_o(1-\eta)/\eta$$

$$P_d = 5.0 \times 10 \times (1-0.92)/0.92 = 4.3478 \text{ Watts}$$

Determine airflow:

Given:  $P_d=4.3478W$  and  $T_a=60^{\circ}\text{C}$

Check above Power de-rating curve:

Airflow  $\leq 200$  ft./min.

Verifying: The maximum temperature rise  $\Delta T = P_d \times R_{ca} = 4.3478 \times 5.4 = 23.48^{\circ}\text{C}$

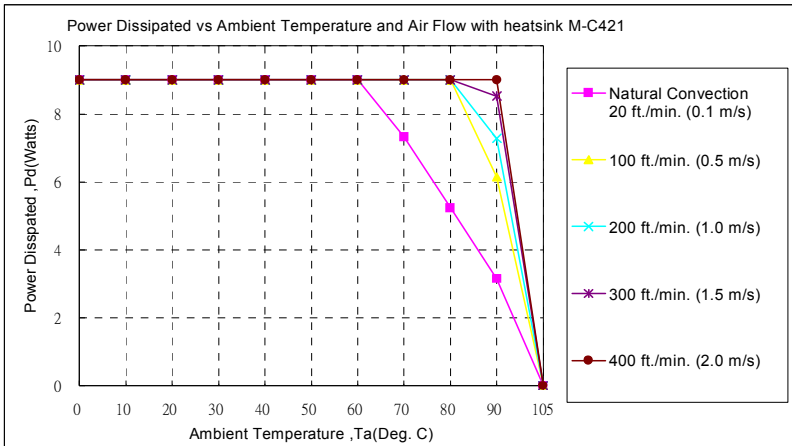
The maximum case temperature  $T_c = T_a + \Delta T = 83.48^{\circ}\text{C} < 105^{\circ}\text{C}$

Where: The  $R_{ca}$  is thermal resistance from case to ambience.

The  $T_a$  is ambient temperature and the  $T_c$  is case temperature



# CQE50W Series Application Note V14 April 2019



AIR FLOW RATE	TYPICAL $R_{ca}$
Natural Convection 20ft./min. (0.1m/s)	4.78 °C/W
100 ft./min. (0.5m/s)	2.44 °C/W
200 ft./min. (1.0m/s)	2.06 °C/W
300 ft./min. (1.5m/s)	1.76 °C/W
400 ft./min. (2.0m/s)	1.58 °C/W

Example with heat sink QBT210 (M-C421):

What is the minimum airflow necessary for a CQE50W-48S12 operating at nominal line voltage, an output current of 4.16A, and a maximum ambient temperature of 60°C?

Solution:

Given:  $V_{in}=48V_{dc}$ ,  $V_o=12V_{dc}$ ,  $I_o=4.16A$

Determine Power dissipation ( $P_d$ ):

$$P_d = P_i - P_o = P_o(1-\eta)/\eta$$

$$P_d = 12 \times 4.16 \times (1-0.92)/0.92 = 4.3478 \text{ Watts}$$

Determine airflow:

Given:  $P_d=4.3478W$  and  $T_a=60^\circ C$

Check above Power de-rating curve:

$P_d < 12.55W$ , Natural Convection

Verify: The maximum temperature rise  $\Delta T = P_d \times R_{ca} = 4.3475 \times 4.78 = 20.8^\circ C$

The maximum case temperature  $T_c = T_a + \Delta T = 80.8^\circ C < 105^\circ C$

Where: The  $R_{ca}$  is thermal resistance from case to ambience.

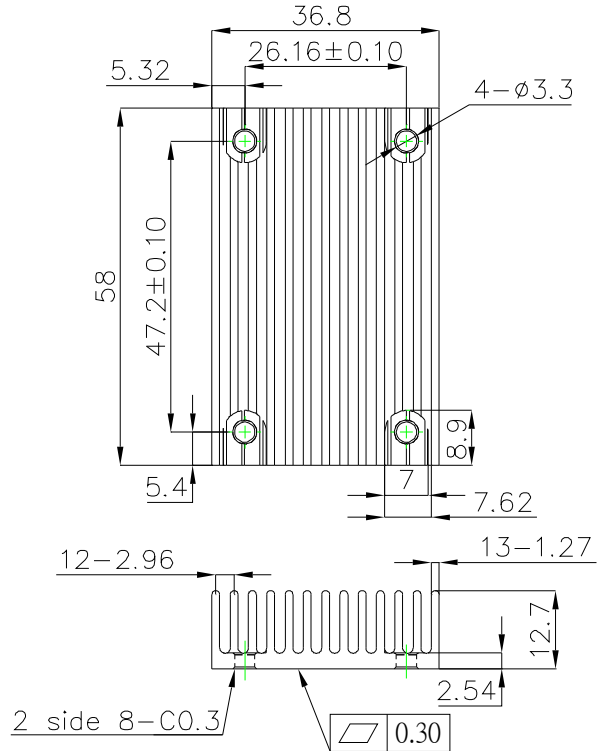
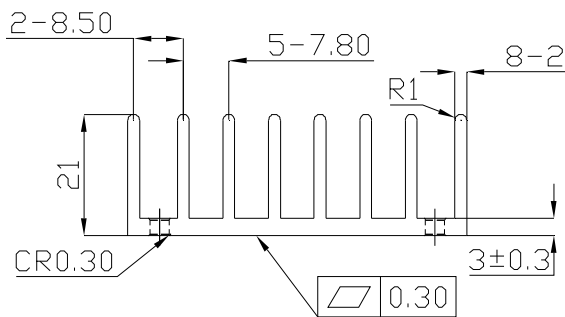
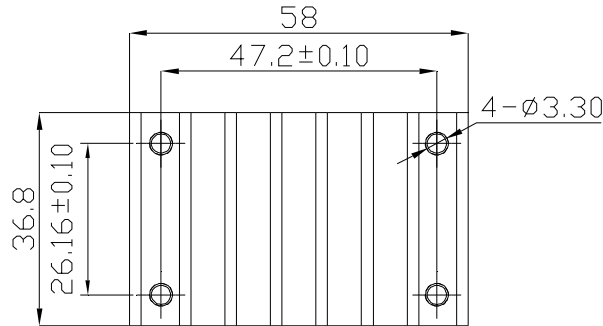
The  $T_a$  is ambient temperature and the  $T_c$  is case temperature.



# CQE50W Series

## Application Note V14 April 2019

### 6.5 Quarter Brick Heat Sinks:



All Dimensions in mm

#### **QBT210 (M-C421): G6620510201**

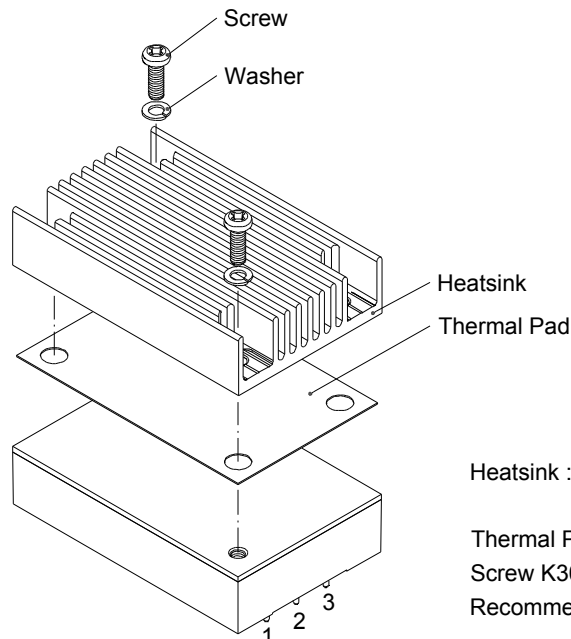
##### **Transverse Heat Sink**

- Rca: 4.78°C/W (typ.), At natural convection
- 2.44°C/W (typ.), At 100LFM
- 2.06°C/W (typ.), At 200LFM
- 1.76°C/W (typ.), At 300LFM
- 1.58°C/W (typ.), At 400LFM

#### **QBL127 (M-C448): G6620570202**

##### **Longitudinal Heat Sink**

- Rca: 5.61°C/W (typ.), At natural convection
- 4.01°C/W (typ.), At 100LFM
- 3.39°C/W (typ.), At 200LFM
- 2.86°C/W (typ.), At 300LFM
- 2.49°C/W (typ.), At 400LFM



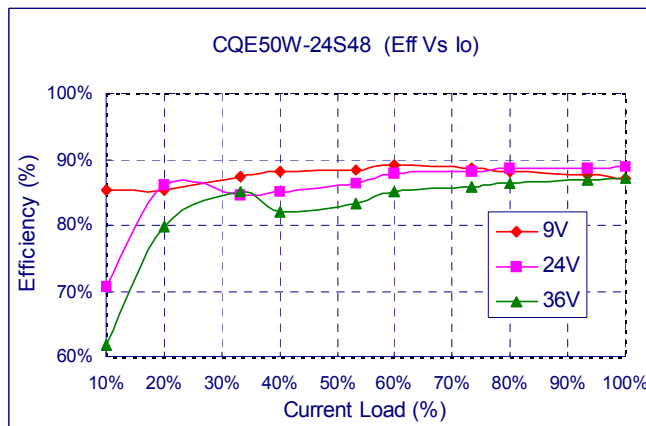
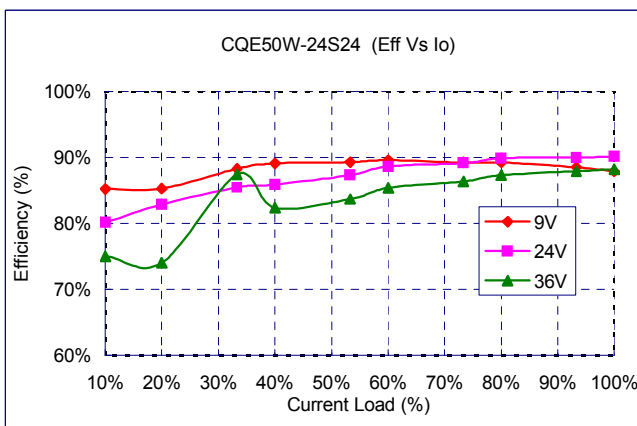
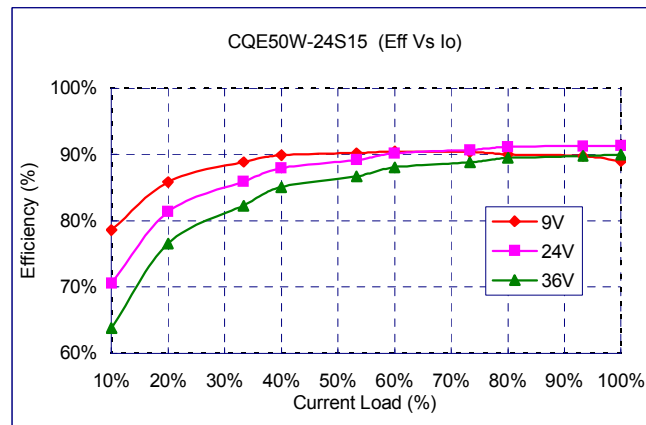
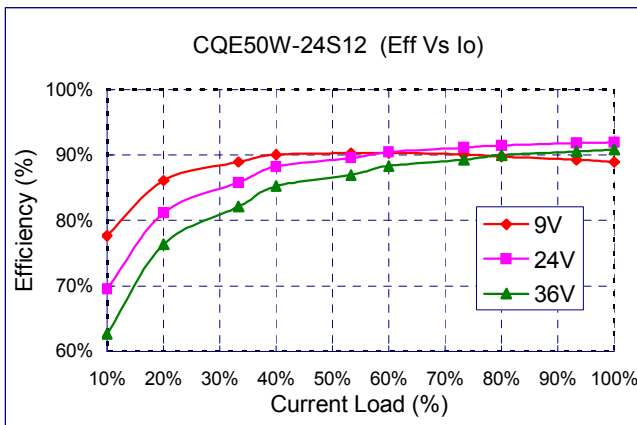
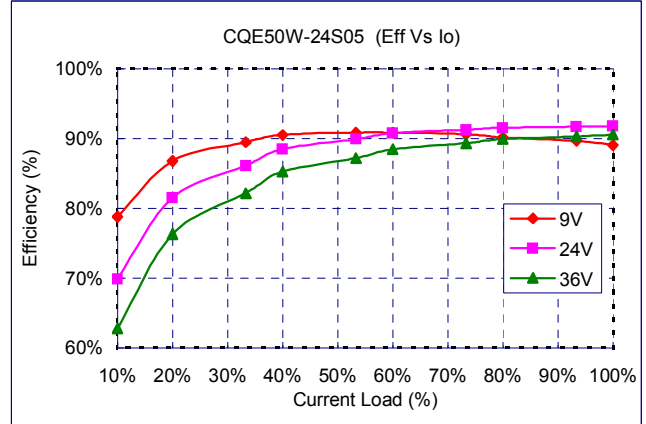
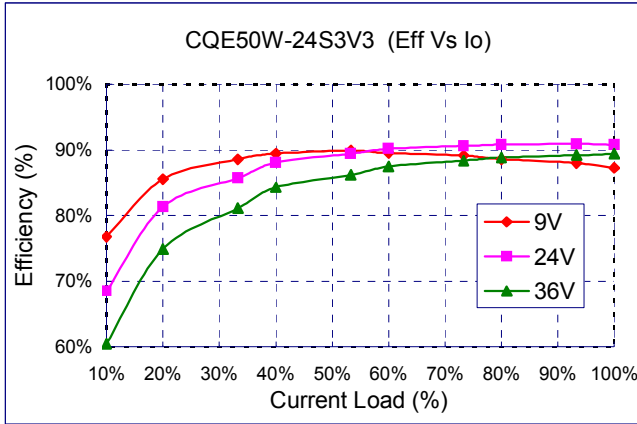
- Heatsink : QBL127 (M-C448)
- QBT210 (M-C421)
- Thermal Pad PQ01: SZ35.8x56.9x0.25mm
- Screw K308W: SMP+SW M3x8L
- Recommended torque 3 Kgf-cm



# CQE50W Series

## Application Note V14 April 2019

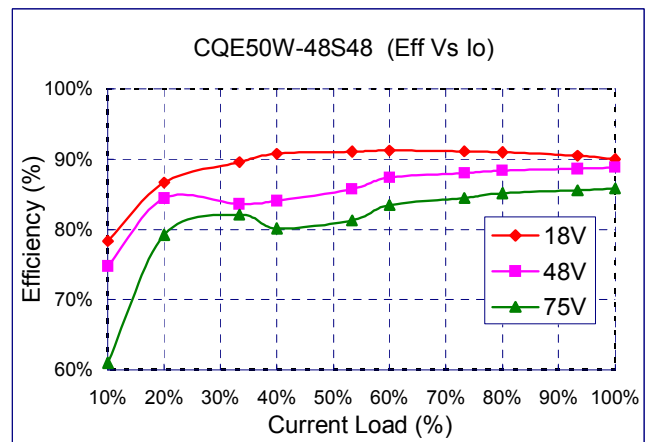
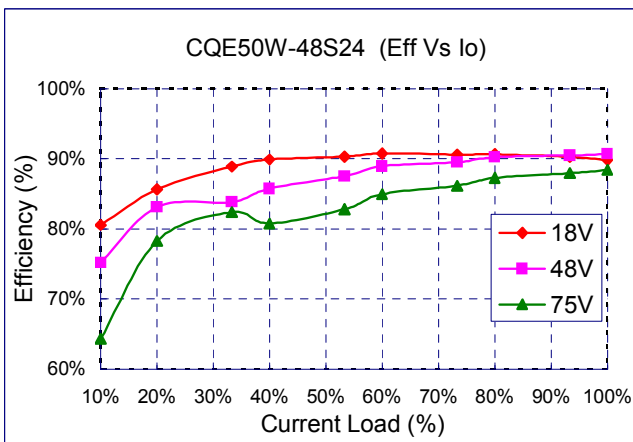
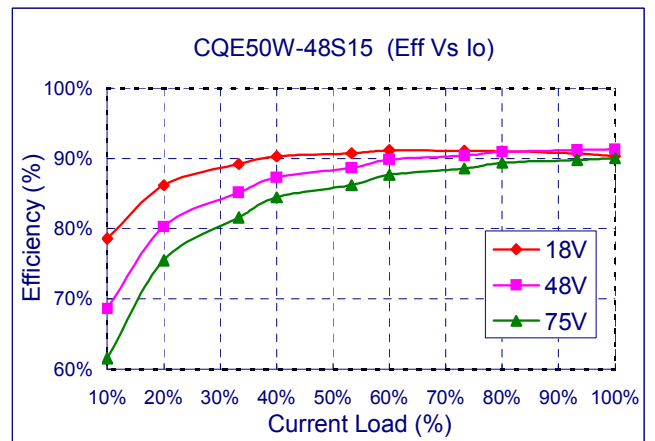
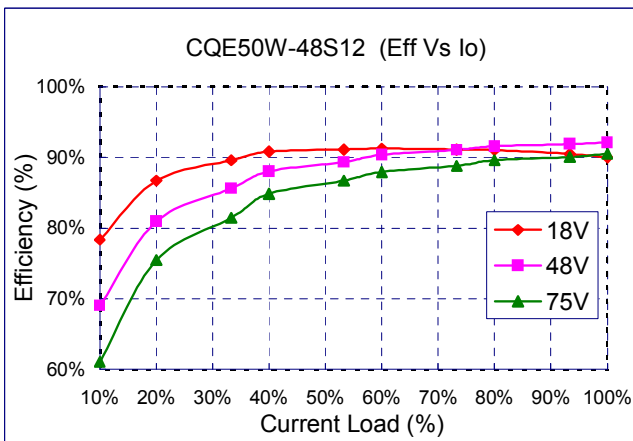
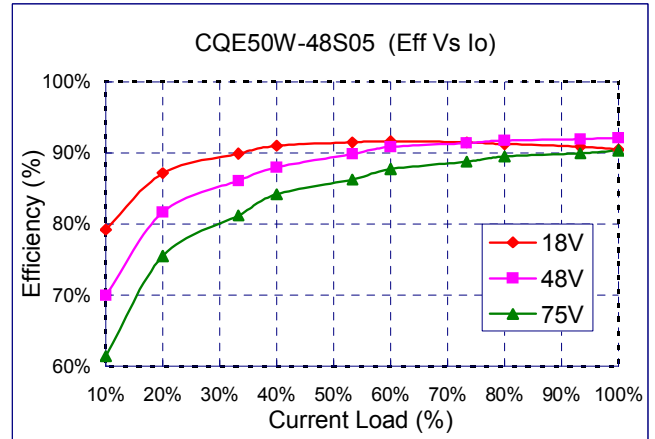
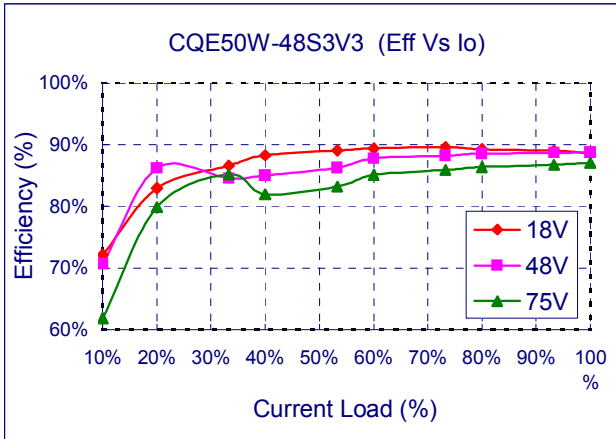
### 6.6 Efficiency VS. Load:





# CQE50W Series

## Application Note V14 April 2019





# CQE50W Series Application Note V14 April 2019

## 6.7 Test Set-Up

The basic test set-up to measure parameters such as efficiency and load regulation is shown below. When testing the modules under any transient conditions please ensure that the transient response of the source is sufficient to power the equipment under test. We can calculate:

- Efficiency
- Load regulation and line regulation.

The value of efficiency is defined as:

$$\eta = \frac{V_o \times I_o}{V_{in} \times I_{in}} \times 100\%$$

Where:

$V_o$  is output voltage,  
 $I_o$  is output current,  
 $V_{in}$  is input voltage,  
 $I_{in}$  is input current.

The value of load regulation is defined as:

$$Load.reg = \frac{V_{FL} - V_{NL}}{V_{NL}} \times 100\%$$

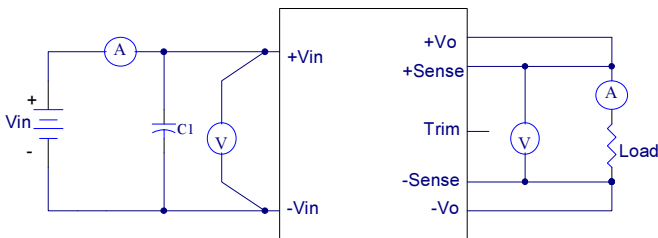
Where:

$V_{FL}$  is the output voltage at full load  
 $V_{NL}$  is the output voltage at no load

The value of line regulation is defined as:

$$Line.reg = \frac{V_{HL} - V_{LL}}{V_{LL}} \times 100\%$$

Where:  $V_{HL}$  is the output voltage of maximum input voltage at full load.  $V_{LL}$  is the output voltage of minimum input voltage at full load.



QCB100W Series Test Setup

## 6.8 Output Voltage Adjustment

In order to trim the voltage up or down one needs to connect the trim resistor either between the trim pin and -Vo for trim-up and between trim pin and +Vo for trim-down. The output voltage trim range is  $\pm 10\%$ . This is shown in Figures 1 and 2:

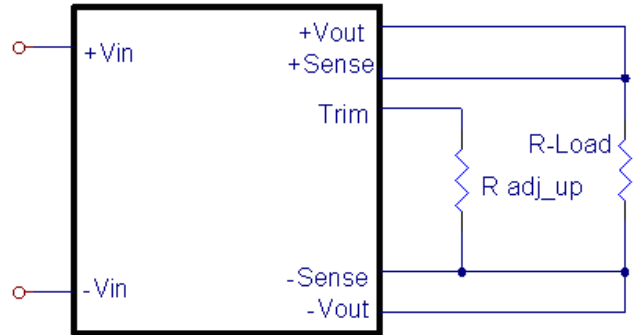


Figure 1. Trim-up Voltage Setup

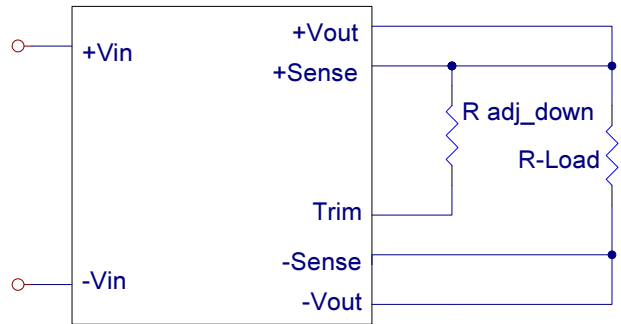


Figure 2. Trim-down Voltage Setup

### 1. The value of Rtrim\_up defined as:

For  $V_o=5V$  Rtrim\_up decision:

$$R_{trim\_up} = \left( \frac{R_1 V_r}{V_o - V_{o\_nom}} \right) - R_2 \quad (K\Omega)$$

For others Rtrim\_up decision:

$$R_{trim\_up} = \left( \frac{R_1 \left( V_r - V_f \left( \frac{R_2}{R_2 + R_3} \right) \right)}{V_o - V_{o\_nom}} \right) - \frac{R_2 R_3}{R_2 + R_3} \quad (K\Omega)$$

Where: R trim\_up is the external resistor in  $K\Omega$ .

$V_{o\_nom}$  is the nominal output voltage.

$V_o$  is the desired output voltage.

$R_1, R_2, R_3$  and  $V_r$  are internal to the unit and are defined in Table 1.

Output Voltage(V)	R1 (K $\Omega$ )	R2 (K $\Omega$ )	R3 (K $\Omega$ )	Vr (V)	Vf (V)
3.3V	3.0	12	4.3	1.24	0.46
5V	2.32	3.3	NC	2.5	0
12V	9.1	51	5.1	2.5	0.46
15V	12	56	8.25	2.5	0.46
24V	20	100	7.5	2.5	0.46
48V	36	270	5.1	2.5	0.46

Table 1 – Trim Resistor Values



# CQE50W Series

## Application Note V14 April 2019

For example, to trim-up the output voltage of 12V module (CQE50W-48S12) by 5% to 12.6V, R trim\_up is calculated as follows:

$$V_o - V_{o\_nom} = 12.6 - 12 = 0.6V$$

$$R1 = 9.1K\Omega, R2 = 51K\Omega, R3 = 5.1K\Omega, V_r = 2.5V, V_f = 0.46$$

$$R_{trim\_up} = \frac{18.944}{0.6} - 4.636 = 26.94 (K\Omega)$$

### 2. The value of R trim\_down defined as:

$$R_{trim\_down} = \frac{R1 \times (V_o - V_r)}{V_{o\_nom} - V_o} - R2 (K\Omega)$$

Where: R trim\_down is the external resistor in Kohm.

V<sub>o\_nom</sub> is the nominal output voltage.

V<sub>o</sub> is the desired output voltage.

R1, R2, R3 and V<sub>r</sub> are internal to the unit and are defined in Table 1.

For example, to trim-down the output voltage of 12V module (CQE50W-48S12) by 5% to 11.4V, R trim-down is calculated as follows:

$$V_{o\_nom} - V_o = 12 - 11.4 = 0.6V$$

$$R1 = 9.1K\Omega, R2 = 51K\Omega, V_r = 2.5V$$

$$R_{trim\_down} = \frac{9.1 \times (11.4 - 2.5)}{0.6} - 51 = 83.98 (K\Omega)$$

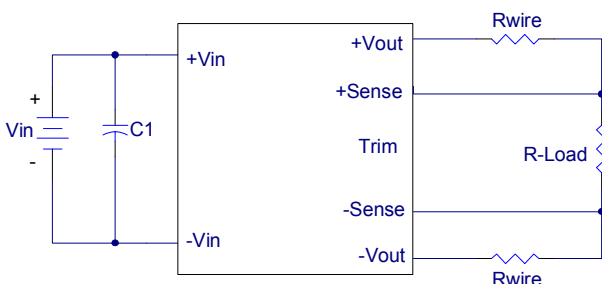
### 6.9 Output Remote Sensing

The CQE50W SERIES converter has the capability to remotely sense both lines of its output. This feature moves the effective output voltage regulation point from the output of the unit to the point of connection of the remote sense pins. This feature automatically adjusts the real output voltage of the CQE50W series in order to compensate for voltage drops in distribution and maintain a regulated voltage at the point of load. The remote-sense voltage range is:

$$[(+V_{out}) - (-V_{out})] - [(+Sense) - (-Sense)] \leq 10\% \text{ of } V_{o\_nominal}$$

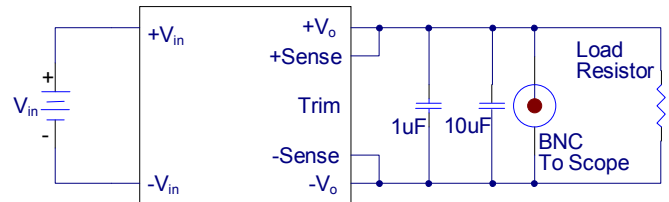
If the remote sense feature is not to be used, the sense pins should be connected locally. The +Sense pin should be connected to the +Vout pin at the module and the -Sense pin should be connected to the -Vout pin at the module.

This is shown in the schematic below.



**Note:** Although the output voltage can be varied (increased or decreased) by both remote sense and trim, the maximum variation for the output voltage is the larger of the two values not the sum of the values. The output power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. Using remote sense and trim can cause the output voltage to increase and consequently increase the power output of the module if output current remains unchanged. Always ensure that the output power of the module remains at or below the maximum rated power. Also be aware that if V<sub>o,set</sub> is below nominal value, P<sub>out,max</sub> will also decrease accordingly because I<sub>o,max</sub> is an absolute limit. Thus, P<sub>out,max</sub> = V<sub>o,set</sub> × I<sub>o,max</sub> is also an absolute limit.

### 6.10 Output Ripple and Noise



Output ripple and noise is measured with 1.0uF ceramic and 10uF solid tantalum capacitors across the output.

(for V<sub>o</sub>: 48V: Output ripple and noise is measured with 1.0uF ceramic and 10uF aluminum capacitors across the output.)

### 6.11 Output Capacitance

The CQE50W series converters provide unconditional stability with or without external capacitors. For good transient response, low ESR output capacitors should be located close to the point of load. PCB design emphasizes low resistance and inductance tracks in consideration of high current applications. Output capacitors with their associated ESR values have an impact on loop stability and bandwidth. These series converters are designed to work with load capacitance to see technical specifications





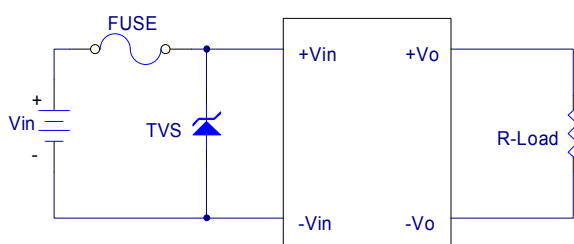
# CQE50W Series

## Application Note V14 April 2019

## 7. Safety & EMC

### 7.1 Input Fusing and Safety Considerations

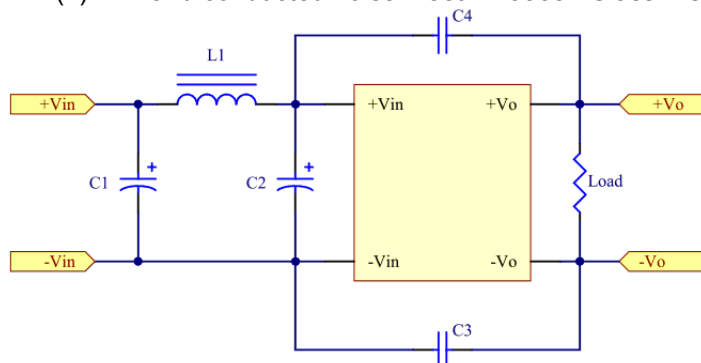
The CQE50W series converters have no internal fuse. In order to achieve maximum safety and system protection, always use an input line fuse. We recommended a 10A time delay fuse for the 24Vin models and a 5A time delay fuse for the 48Vin models. It is recommended that the circuit have a transient voltage suppressor diode (TVS) across the input terminal to protect the unit against surge or spike voltage and input reverse voltage (as shown).



### 7.2 EMC Considerations

#### Suggested Circuits for Conducted EMI Class A & Class B

(1) EMI and conducted noise meet EN55032 Class A specifications:



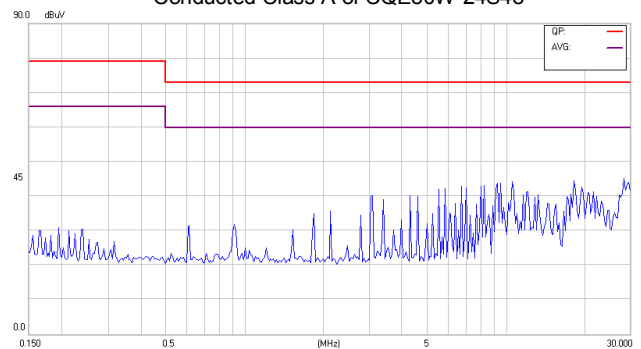
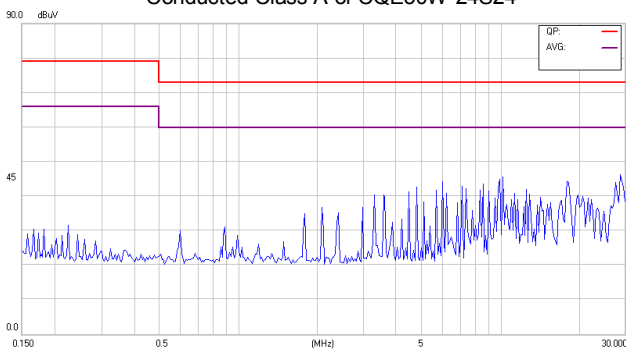
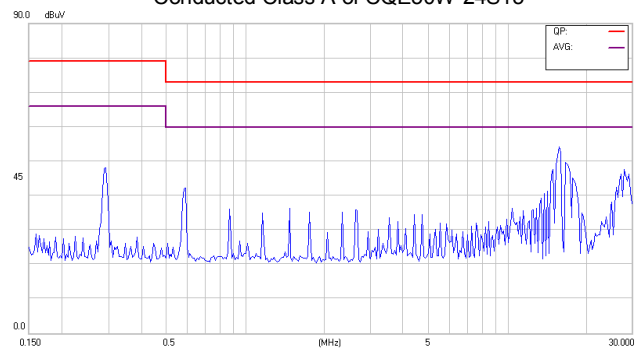
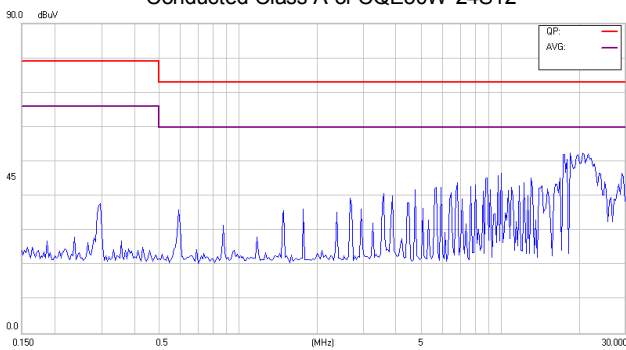
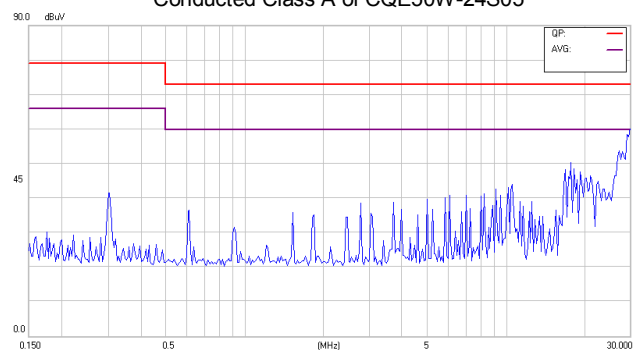
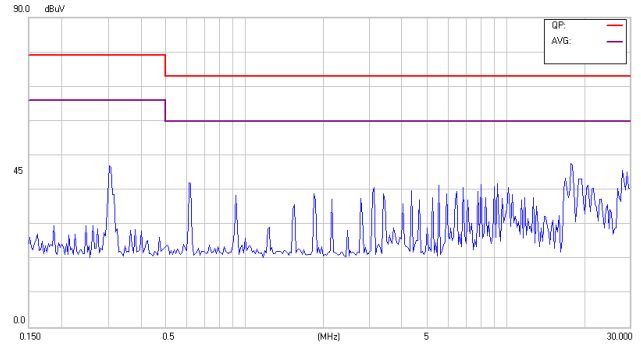
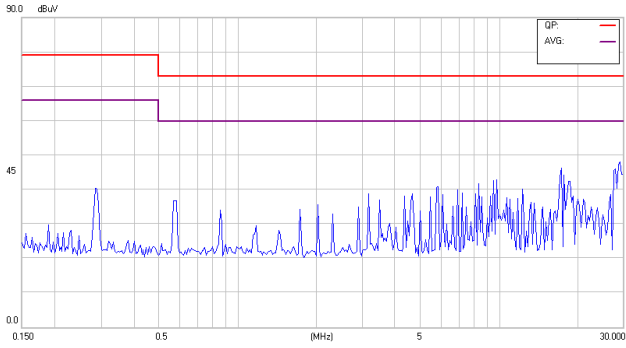
Model No.	C1	C2	C3	C4	L1
CQE50W-24S3v3	220u/63V ESR < 0.046Ω	220u/63V ESR < 0.046Ω	2200pF/2KV	2200pF/2KV	3.4uH
CQE50W-24S05	220u/63V ESR < 0.046Ω	220u/63V ESR < 0.046Ω	2200pF/2KV	2200pF/2KV	3.4uH
CQE50W-24S12	220u/63V ESR < 0.046Ω	220u/63V ESR < 0.046Ω	2200pF/2KV	2200pF/2KV	3.4uH
CQE50W-24S15	220u/63V ESR < 0.046Ω	220u/63V ESR < 0.046Ω	2200pF/2KV	2200pF/2KV	3.4uH
CQE50W-24S24	220u/63V ESR < 0.046Ω	220u/63V ESR < 0.046Ω	2200pF/2KV	2200pF/2KV	3.4uH
CQE50W-24S48	220u/63V ESR < 0.046Ω	220u/63V ESR < 0.046Ω	2200pF/2KV	2200pF/2KV	3.4uH
CQE50W-48S3v3	82u/100V ESR < 0.084Ω	82u/100V ESR < 0.084Ω	2200pF/2KV	2200pF/2KV	3.4uH
CQE50W-48S05	82u/100V ESR < 0.084Ω	82u/100V ESR < 0.084Ω	2200pF/2KV	2200pF/2KV	3.4uH
CQE50W-48S12	82u/100V ESR < 0.084Ω	82u/100V ESR < 0.084Ω	2200pF/2KV	2200pF/2KV	3.4uH
CQE50W-48S15	82u/100V ESR < 0.084Ω	82u/100V ESR < 0.084Ω	2200pF/2KV	2200pF/2KV	3.4uH
CQE50W-48S24	82u/100V ESR < 0.084Ω	82u/100V ESR < 0.084Ω	2200pF/2KV	2200pF/2KV	3.4uH
CQE50W-48S48	82u/100V ESR < 0.084Ω	82u/100V ESR < 0.084Ω	2200pF/2KV	2200pF/2KV	3.4uH

Note: The C1 and C2 are aluminum capacitors, C3 and C4 are ceramic capacitors.



# CQE50W Series

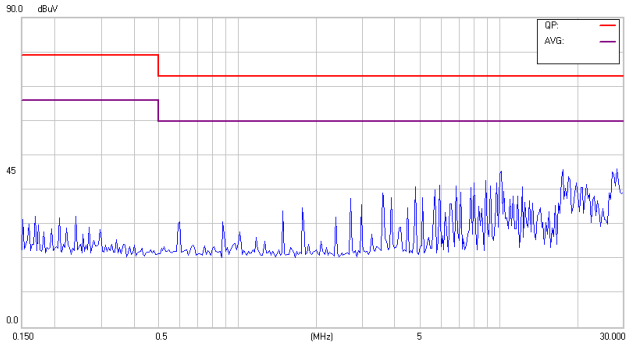
## Application Note V14 April 2019



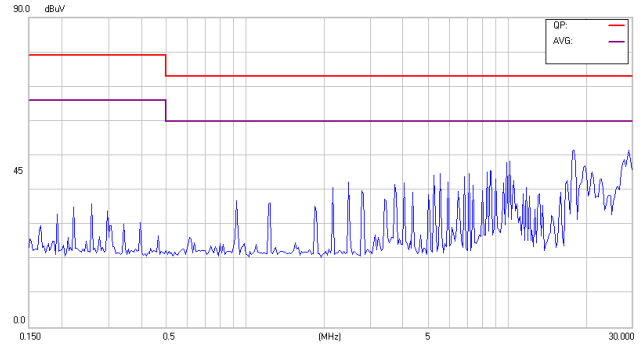


# CQE50W Series

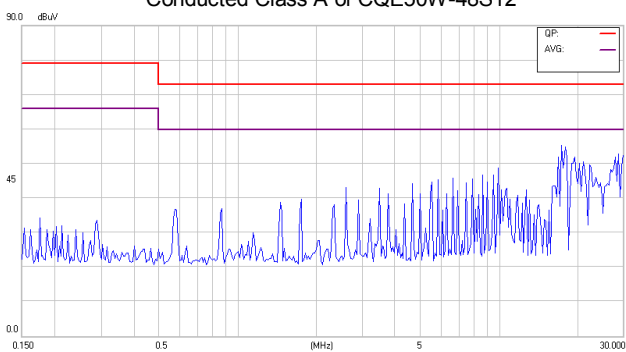
## Application Note V14 April 2019



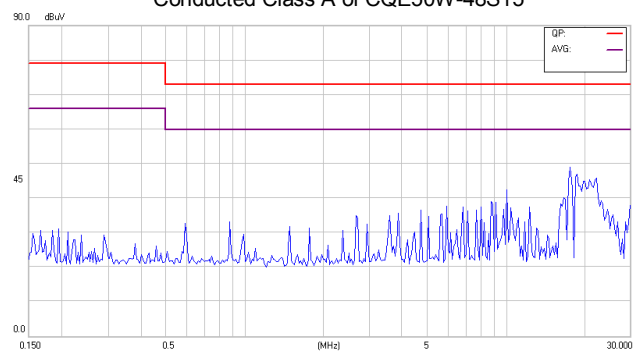
Conducted Class A of CQE50W-48S12



Conducted Class A of CQE50W-48S15



Conducted Class A of CQE50W-48S24

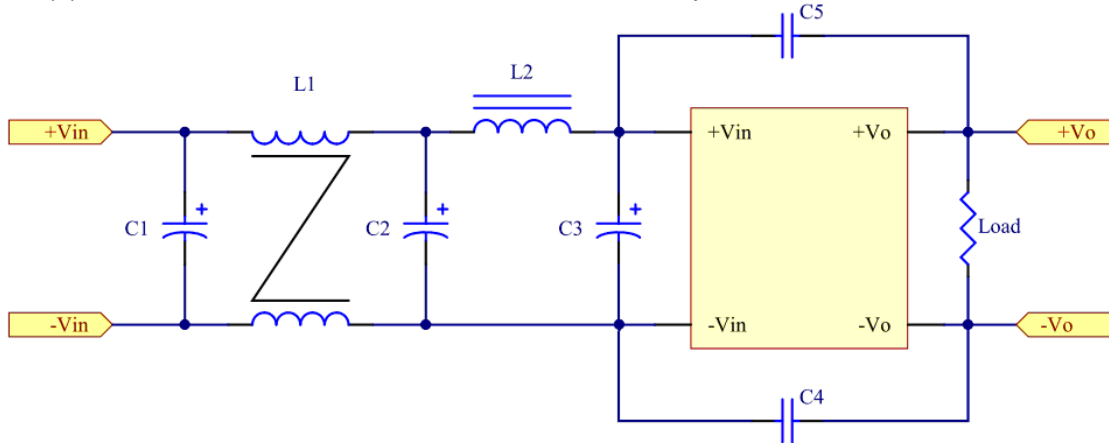


Conducted Class A of CQE50W-48S48



## CQE50W Series Application Note V14 April 2019

(2) EMI and conducted noise meet EN55032 Class B specifications:



Model No.	C1	C2	C3	C4	C5	L1	L2
CQE50W-24S3V3	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	1000pF/3KV	1000pF/3KV	0.5mH	12uH
CQE50W-24S05	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	1000pF/3KV	1000pF/3KV	0.5mH	12uH
CQE50W-24S12	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	3300pF/2KV	3300pF/2KV	0.5mH	12uH
CQE50W-24S15	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	3300pF/2KV	3300pF/2KV	0.5mH	12uH
CQE50W-24S24	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	3300pF/2KV	3300pF/2KV	0.5mH	12uH
CQE50W-24S48	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	100u/50V ESR < 0.33Ω	3300pF/2KV	3300pF/2KV	0.5mH	12uH
CQE50W-48S3V3	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	3300pF/2KV	3300pF/2KV	0.5mH	12uH
CQE50W-48S05	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	3300pF/2KV	3300pF/2KV	0.5mH	12uH
CQE50W-48S12	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	3300pF/2KV	3300pF/2KV	0.5mH	12uH
CQE50W-48S15	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	3300pF/2KV	3300pF/2KV	0.5mH	12uH
CQE50W-48S24	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	3300pF/2KV	3300pF/2KV	0.5mH	12uH
CQE50W-48S48	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	47u/100V ESR < 0.17Ω	3300pF/2KV	3300pF/2KV	0.5mH	12uH

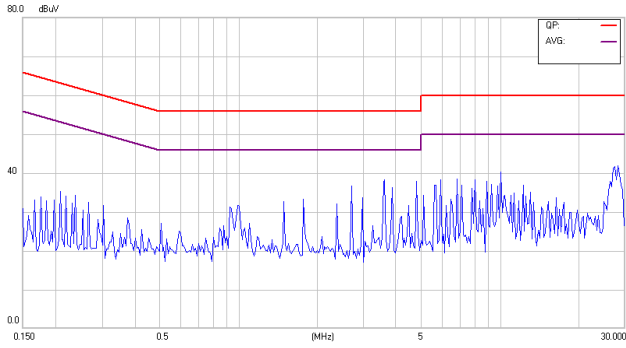
Note: The C1, C2 and C3 are aluminum capacitors, C4 and C5 are ceramic capacitors



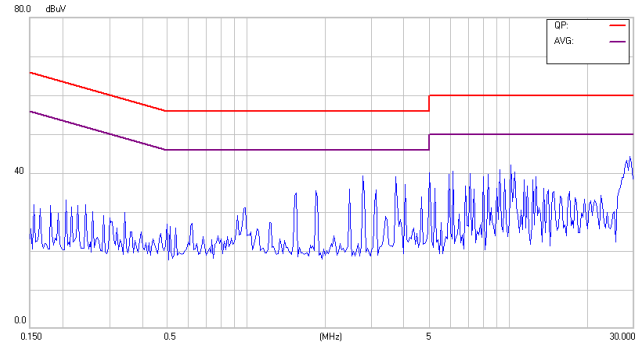


# CQE50W Series

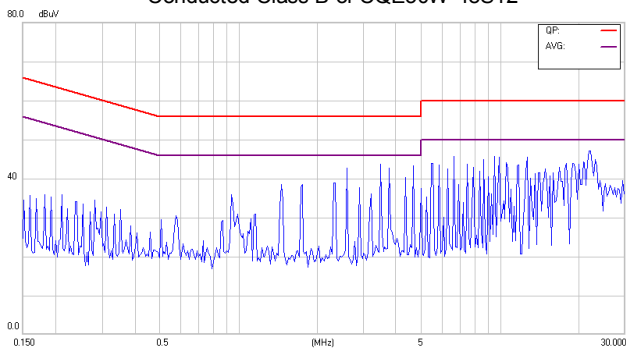
## Application Note V14 April 2019



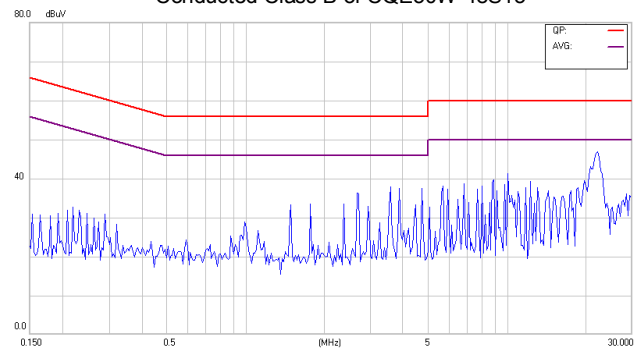
Conducted Class B of CQE50W-48S12



Conducted Class B of CQE50W-48S15



Conducted Class B of CQE50W-48S24



Conducted Class B of CQE50W-48S48



# CQE50W Series

## Application Note V14 April 2019

### 8. Part Number

Format: **CQE50W** - II X 00 L

Parameter	Series	Nominal Input Voltage	Number of Outputs	Output Voltage	Remote ON/OFF Logic
Symbol	CQE50W	II	X	00	L
Value	CQE50W	24: 24 Volts 48: 48 Volts	S: Single	3V3: 3.3 Volts 05: 05 Volts 12: 12 Volts 15: 15 Volts 24: 24 Volts 48: 48 Volts	None: Positive N: Negative

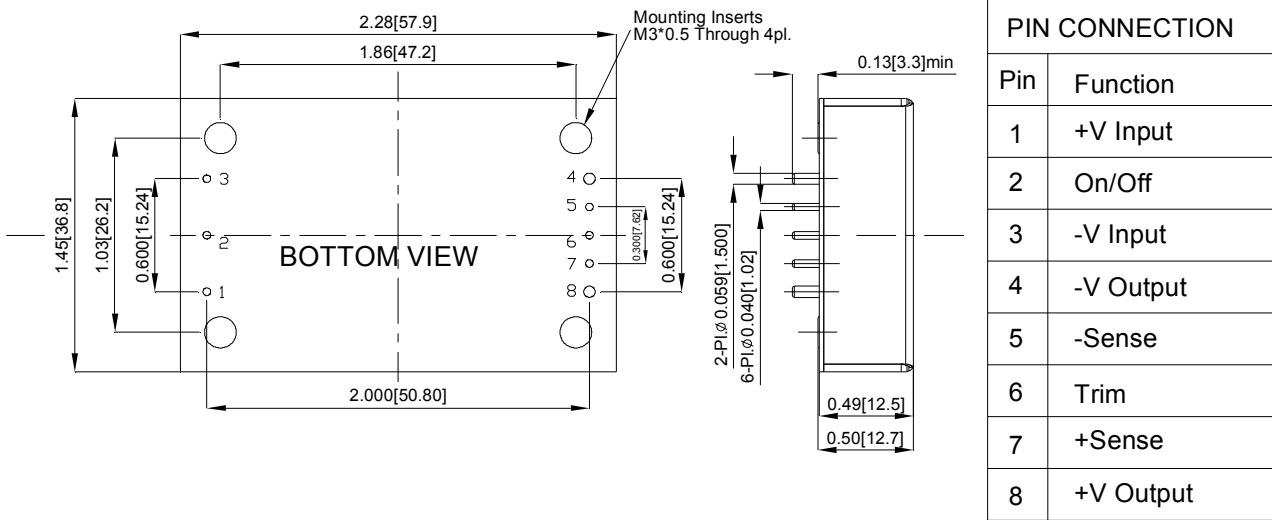
### 9. Mechanical Specifications

#### 9.1 Mechanical Outline Diagrams

CASE QB

All Dimensions In Inches(mm)

Tolerances Inches: X.XX= ±0.02 , X.XXX= ±0.010  
 Millimeters: X.X= ±0.5 , X.XX=±0.25



CQE50W Mechanical Outline Diagram

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**Factory:**

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 Fu Hsing Hsiang,  
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