

Application Note V10 August 2014

## ISOLATED DC-DC Converter EC4AW SERIES APPLICATION NOTE



### Approved By:

Department	Approved By	Checked By	Written By
Research and Development Department	Enoch	Danny	Mike
Quality Assurance Department	Jack	Benny	



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### 1. Introduction

The EC4AW series offer 3.3-6watts of output power in a 24 pin DIP and SMD metal package. The EC4AW series has a 4:1 wide input voltage range of 9-36VDC, 18-72VDC, and provides a precisely regulated output. This series has features such as high efficiency, 1500VDC of isolation and allows an ambient operating temperature range of -40°C to 85°C (de-rating above 71 °C). The modules are fully protected against output short circuit. All models are very suitable for distributed power architectures, telecommunications, battery operated equipment and industrial applications.

### 2. DC-DC Converter Features

- \* 3.3-6W Isolated Output
- \* Efficiency to 83%
- \* 4:1 Input Range
- \* Regulated Outputs
- \* Pi Input Filter
- \* DIP-24 / SMD Metal Package
- \* Continuous Short Circuit Protection
- \* No Tantalum Capacitor Inside

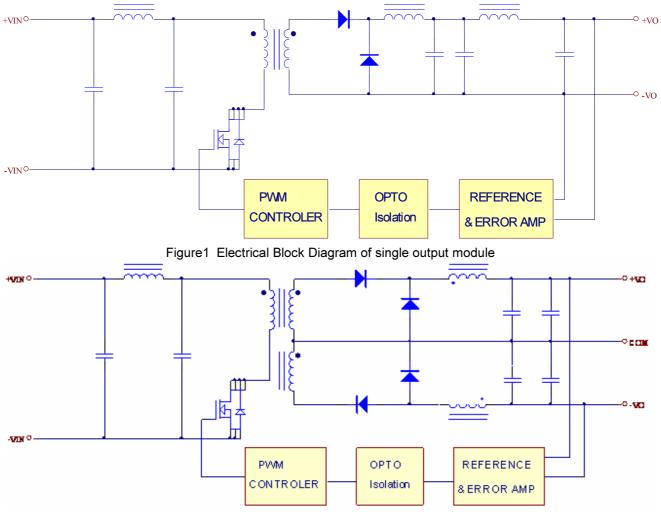


Figure2 Electrical Block Diagram of dual output module

### 3. Electrical Block Diagram



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### 4. Technical Specifications

(All specifications are typical at nominal input, full load at 25°C unless otherwise noted.)

#### ABSOLUTE MAXIMUM RATINGS

PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units	
Input Voltage							
Continuous		EC4AW0X	-0.7		36	Vdc	
Continuous		EC4AW1X	-0.7		72	vuc	
Transiant	100ms	EC4AW0X			50	Vdc	
Transient	Tooms	EC4AW1X			100		
Operating Ambient Temperature	With de-rating, above $71^{\circ}$ C	All	-40		+85	°C	
Case Temperature		All			+100	°C	
Storage Temperature		All	-40		+100	°C	
		EC4AWXX	1500			Vdc	
Input/Output Isolation Voltage	1 minute	(HM/HMS)					
		EC4AWXXH	3000			Vdc	
INPUT CHARACTERISTIC	S	1	r	1			
PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units	
Operating Input Valtage		EC4AW0X	9	24	36	Vda	
Operating Input Voltage		EC4AW1X	18	48	72	Vdc	
Maximum Innut Current	Full Load, Vin= 9V	EC4AW0X		825			
Maximum Input Current	Full Load, Vin=18V	EC4AW1X		420		mA	
		EC4AW01		5			
		EC4AW02		5			
		EC4AW03		5			
		EC4AW04		7.5		mA	
		EC4AW05		7.5			
		EC4AW06		5			
		EC4AW07		5			
No-Load Input Current	Vin=Nominal input	EC4AW11		5			
		EC4AW12		5			
		EC4AW13		5			
		EC4AW14		7.5			
		EC4AW15		7.5			
		EC4AW16		5			
		EC4AW17		5			
Inrush Current (I <sup>2</sup> t)	As per ETS300 132-2	All			0.0003	A <sup>2</sup> s	
Input Reflected-Ripple Current	P-P thru 12uH inductor, 5Hz to20MHz	All		10		mA	
OUTPUT CHARACTERIST					l		
		During	N.4.	<b>T</b> · ·		11.24	
PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units	
		Vo=5Vdc	4.9	5	5.1		
		Vo=12Vdc	11.76	12	12.24		
	Vin=Nominal Vin, Io=Io max.,	Vo=15Vdc	14.7	15	15.3		
Output Voltage Set Point	Tc=25℃	Vo=±5Vdc	±4.9	±5	±5.1	Vdc	
		Vo=±12Vdc	±11.76	±12	±12.24		
		Vo=±15Vdc	±14.7	±15	±15.3		
	1	1/- 0 01/-1-	0.004				

Vo=3.3Vdc

3.234

3.3

3.366



Output Voltage Balance	Vin=nominal, Io=Iomax, Tc=25℃	Dual			±1.0	%
Output Voltage Regulation						
Lood Dogulation	Io=Full Load to 10% Load	Single			±0.5	%
Load Regulation	Io=Full Load to 25% Load	Dual			±1.0	70
Line Regulation	Vin= high line to low line, Full Load	All			±0.5	%
Temperature Coefficient	Ta=-40°C to 85°C	All			±0.05	<b>%/</b> °C
Output Voltage Ripple and Noise						
		Vo=5Vdc				
		Vo=12Vdc				
	Vin-nominal input lat full load	Vo=15Vdc				
Peak-to-Peak	Vin=nominal input, lo= full load 20MHz bandwidth	Vo=±5Vdc			100	mV
		Vo=±12Vdc				
		Vo=±15Vdc				
		Vo=3.3Vdc				
		Vo=5Vdc	0		1000	
		Vo=12Vdc	0		470	
		Vo=15Vdc	0		400	_
Operating Output Current Range		Vo=±5Vdc	0		±500	mA
		Vo=±12Vdc	0		±230	
		Vo=±15Vdc	0		±190	
		Vo=3.3Vdc	0		1000	
Output DC Current-Limit Inception	Output Voltage =90% Nominal Output Voltage	All	120			%
		Vo=5Vdc	0		1000	
		Vo=12Vdc	0		470	
		Vo=15Vdc	0		400	. F
Maximum Output Capacitance	Full load, Resistance	Vo=±5Vdc	0 0		500	uF
		Vo=±12Vdc	0		230	
		Vo=±15Vdc	0		190	
		Vo=3.3Vdc	0		1000	
DYNAMIC CHARACTERIS						1
PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
Start up Time						
Turn-On Delay Time, From Input	Vin, min. to 10%Vo,set	All		15		ms
Output Voltage Rise Time	10%Vo,set to 90%Vo,set	All		15		ms
EFFICIENCY						
PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
		EC4AW01		82		
		EC4AW02		83		
		EC4AW03		83		
100% Load	Vin=Nominal Vin, Io=Io.max, Tc=25℃	EC4AW04		82		%
		EC4AW05		81		
		EC4AW06		83		
		EC4AW07		78		



EFFICIENCY				11		
PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
		EC4AW11		79		
		EC4AW12		82		
	Vin=Nominal Vin, Io=Io.max,	EC4AW13		81		
	Tc=25 $^{\circ}$ C	EC4AW14		80		
		EC4AW15		80		
		EC4AW16		80		
		EC4AW17		74		
		EC4AW01		84		
		EC4AW02		85		
		EC4AW03		85		
100% Load		EC4AW04		85		%
		EC4AW05		85		
		EC4AW06		85		
	Vin=1/2Nominal Vin, Io=Io.max,	EC4AW07		80		_
	Tc=25℃	EC4AW11		83		
		EC4AW12		86		
		EC4AW13		86		
		EC4AW14		85		
		EC4AW15		85		
		EC4AW16		84		
		EC4AW17		79		
ISOLATION CHARAC	TERISTICS					
PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
		EC4AWXX			1500	Vdc
Isolation Voltage	Input to Output 1 minutes	(HM/HMS)				
		EC4AWXXH			3000	Vdc
solation Resistance	Input to Output	All			1000	MΩ
Isolation Capacitance	Input to Output	EC4AWXX (HM/HMS)		560		pF
Isolation Capacitance		EC4AWXXH		280		pF
FEATURE CHARACTE	ERISTICS					
PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
		Single		300		KHz
Switching Frequency	Vin=Nominal, Io=Io.max	Dual		230		KHz
GENERAL SPECIFICA	TIONS	· ·		· ·		•
PARAMETER	NOTES and CONDITIONS	Device	Min.	Typical	Max.	Units
	lo=100%of lo.max;Ta=25℃ per	Single		TBD		<b>.</b>
MTBF	MIL-HDBK-217F	Dual		TBD		- Mhours
Weight		All		12.5		grams



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#### **5. Main Features and Functions** *5.1 Operating Temperature Range*

The EC4AW series converters can be operated by a wide ambient temperature range from -40°C to 85°C (de-rating above 71 °C). The standard model has a Copper case and case temperature can not over 100°C at normal operating.

#### 5.2 Over Current Protection

All different voltage models have full continuous short-circuit protection. To provide protection in a fault condition, the unit is equipped with internal over-current protection. The unit operates normally once the fault condition is removed. At the point of current-limit inception, the converter will go into over current protection.

#### 6. Applications

# 6.1 Recommended Layout PCB Footprints and Soldering Information

The system designer or the end user must ensure that other components and metal in the vicinity of the converter meet the spacing requirements to which the system is approved. Low resistance and low inductance PCB layout traces are the norm and should be used where possible. Due consideration must also be given to proper low impedance tracks between power module, input and output grounds. The recommended footprints and soldering profiles are shown as Figure 3~6.

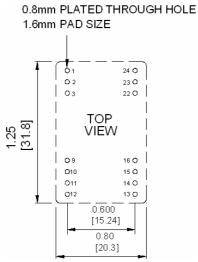
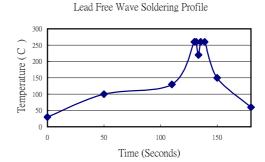




Figure3 Recommended PCB Layout Footprints for DIP-24 packages



Note :

- 1. Soldering Materials: Sn/Cu/Ni
- 2. Ramp up rate during preheat: 1.4 °C/Sec (From 50°C to 100°C)
- 3. Soaking temperature: 0.5 ℃/Sec (From 100℃ to 130℃), 60±20 seconds
- 4. Peak temperature: 260°C, above 250°C 3~6 Seconds
- 5. Ramp up rate during cooling: -10.0  $^{\circ}$ C/Sec (From 260 $^{\circ}$ C to 150 $^{\circ}$ C)
- Figure4 Recommended Wave Soldering Profiles for DIP-24 packages

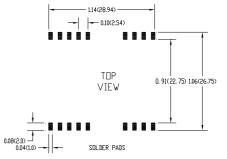
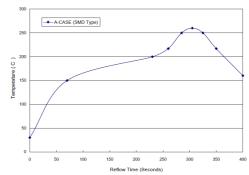


Figure5 Recommended PCB Layout Footprints for SMD packages Lead Free Hot Air Reflow Profile



- 1. Soldering Paste: SHENMAO PF610-P (Sn/Ag/Cu)
- 2. Ramp up rate during preheat: 1.71  $^\circ\! C/Sec$  (From 30  $^\circ\! C$  to 150  $^\circ\! C$  )
- 3. Soaking temperature: 0.31  $^\circ \! \mathbb{C}/Sec$  (From 150 $^\circ \! \mathbb{C}$  to 200 $^\circ \! \mathbb{C}$  ), 160±10 seconds
- 4. Ramp up rate during reflow: 0.96  $^\circ \! {\rm C}/{\rm Sec}$  (From 217  $^\circ \! {\rm C}$  to 260  $^\circ \! {\rm C}$  )
- 5. Peak temperature: 260  $^\circ\!\mathrm{C}$  , above 217  $^\circ\!\mathrm{C}$  90 Seconds
- 6. Ramp up rate during cooling: -1.2 °C /Sec (From 260°C to 160°C) Figure6 Recommended Air Reflow Profiles for SMD packages

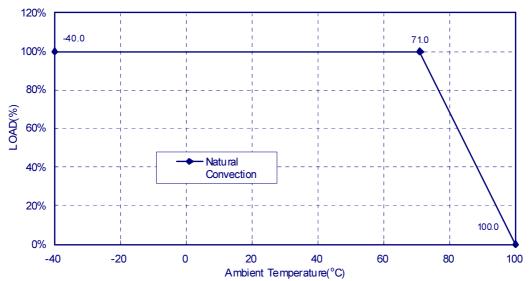


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#### 6.2 Power De-Rating Curves for EC4AW Series

Operating Ambient temperature Range: -40  $^\circ C$  ~ 85  $^\circ C$  with de-rating above 71  $^\circ C$  .

Maximum case temperature under any operating condition should not exceed 100  $^\circ\!\mathrm{C}.$ 



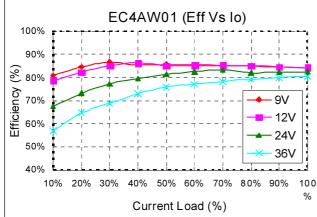
#### Typical Derating curve for Natural Convection



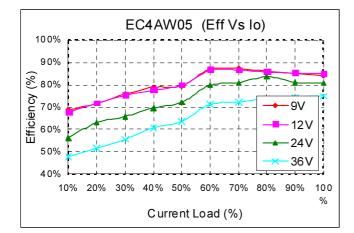
6.3 Efficiency vs. Load Curves

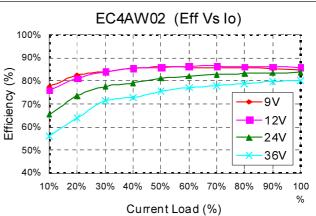
### EC4AW 3.3-6W Isolated DC-DC Converters

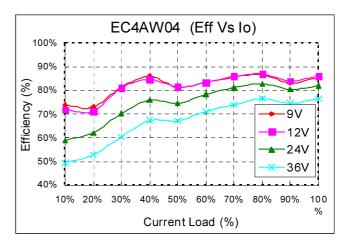
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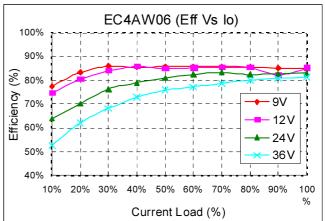


#### EC4AW03 (Eff Vs lo) 100% 90% Efficiency (%) 80% 70% 91/ 12V 60% 24V 50% 36V 40% 10% 20% 30% 40% 50% 60% 70% 80% 90% 100 % Current Load (%)

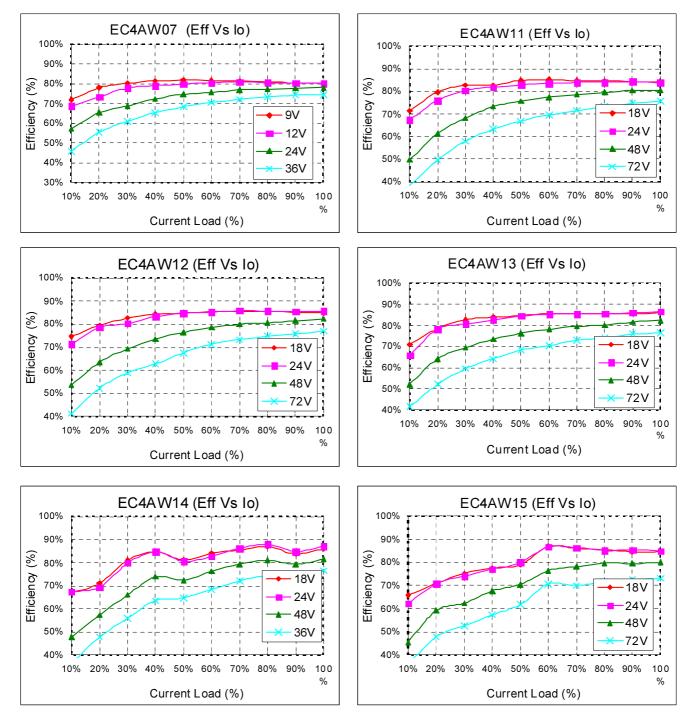




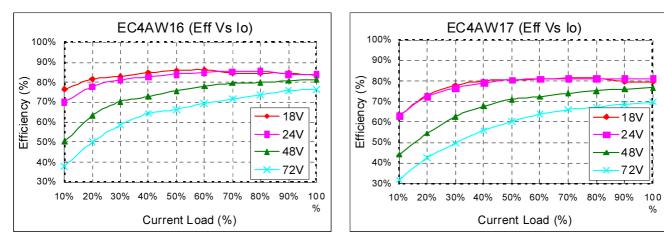










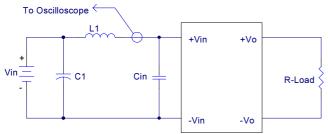




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#### 6.4 Input Capacitance at the Power Module

The converters must be connected to low AC source impedance. To avoid problems with loop stability source inductance should be low. Also, the input capacitors (Cin) should be placed close to the converter input pins to de-couple distribution inductance. However, the external input capacitors are chosen for suitable ripple handling capability. Low ESR capacitors are good choice. Circuit as shown in Figure7 represents typical measurement methods for reflected ripple current. C1 and L1 simulate a typical DC source impedance. The input reflected-ripple current is measured by current probe to oscilloscope with a simulated source Inductance (L1).



L1: 12uH.

C1: 47uF ESR <0.17  $\Omega$  @ 20  $^\circ\!\mathrm{C}$  , 100KHz. Cin: None

Figure7 Input Reflected-Ripple Test Setup

#### 6.5 Test Set-Up

The basic test set-up to measure parameters such as efficiency and load regulation is shown in Figure8. When testing the modules under any transient conditions please ensure that the transient response of the source is sufficient to power the equipment under test. We can calculate the

Efficiency

Load regulation and line regulation.

The value of efficiency is defined as:

$$\eta = \frac{Vo \times Io}{Vin \times Iin} \times 100\%$$

Where

Vo is output voltage, lo is output current, Vin is input voltage, lin is input current.

The value of load regulation is defined as:

$$Load.reg = \frac{V_{FL} - V_{NL}}{V_{NL}} \times 100\%$$

Where

V<sub>FL</sub> is the output voltage at full load

 $V_{NL}$  is the output voltage at 10% load (Single output)  $V_{NL}$  is the output voltage at 25% load (Dual output)

The value of line regulation is defined as:

$$Line.reg = \frac{V_{HL} - V_{LL}}{V_{LL}} \times 100\%$$

Where

- $V_{\text{HL}} \text{ is the output voltage of maximum input} \\ \text{voltage at full load.}$
- V<sub>LL</sub> is the output voltage of minimum input voltage at full load

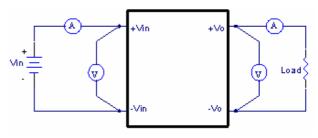


Figure8 EC4AW Series Test Setup

#### 6.6 Output Ripple and Noise Measurement

The test set-up for noise and ripple measurements is shown in Figure9 and Figure10. A coaxial cable was used to prevent impedance mismatch reflections disturbing the noise readings at higher frequencies. Measurements are taken with output appropriately loaded and all ripple/noise specifications are from D.C. to 20MHz Band Width.

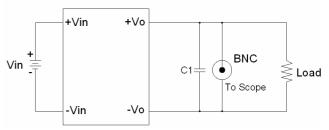


Figure9 Using BNC to Measure Output Ripple and Noise

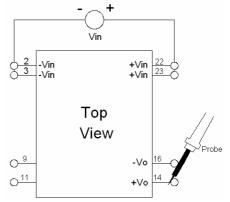


Figure 10 Using Probe to Measure Output Ripple and Noise



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#### 6.7 Output Capacitance

The EC4AW series converters provide unconditional stability with or without external capacitors. For good transient response low ESR output capacitors should

be located close to the point of load. These series converters are designed to work with load capacitance to see technical specifications.

### 7. Safety & EMC

#### 7.1 Input Fusing and Safety Considerations.

The EC4AW series converters have not an internal fuse. However, to achieve maximum safety and system protection, always use an input line fuse. We recommended a time delay fuse 1A for 24Vin models and 0.5A for 48Vin modules. Figure11 circuit is recommended by a Transient Voltage Suppressor diode across the input terminal to protect the unit against surge or spike voltage and input reverse voltage.

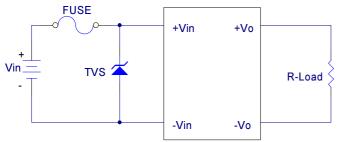


Figure11 Input Protection

#### 7.2 EMC Considerations

EMI Test standard: EN55022 Test Condition: Input Voltage: Nominal, Output Load: Full Load

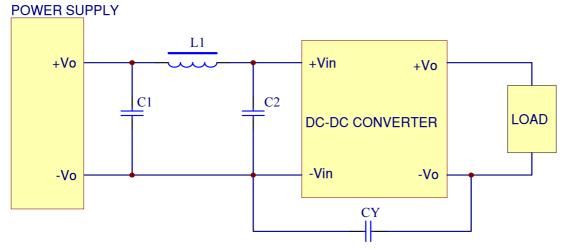


Figure12 Connection circuit for conducted EMI testing



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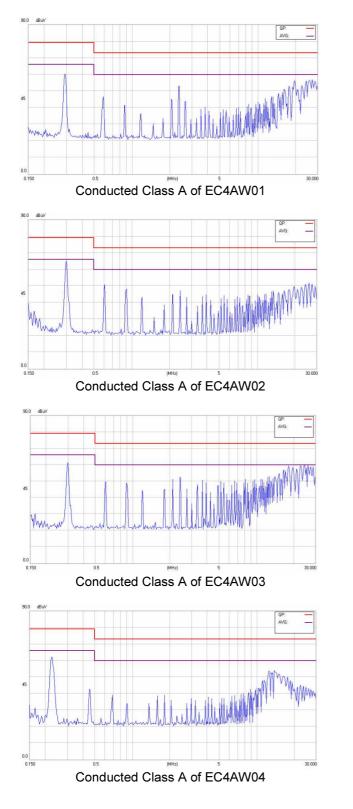
Γ		EN5502	22 Class A	
Model No.	C1	C2	CY	L1
EC4AW01	NC	120uF/100V	NC	SHORT
EC4AW02	NC	120uF/100V	NC	SHORT
EC4AW03	NC	120uF/100V	NC	SHORT
EC4AW04	NC	120uF/100V	NC	SHORT
EC4AW05	NC	120uF/100V	NC	1uH
EC4AW06	NC	120uF/100V	NC	SHORT
EC4AW07	NC	120uF/100V	NC	SHORT
EC4AW11	NC	120uF/100V	NC	SHORT
EC4AW12	NC	120uF/100V	NC	SHORT
EC4AW13	NC	120uF/100V	NC	SHORT
EC4A1W4	NC	120uF/100V	NC	SHORT
EC4AW15	NC	120uF/100V	NC	1uH
EC4AW16	NC	120uF/100V	NC	SHORT
EC4AW17	NC	120uF/100V	NC	SHORT

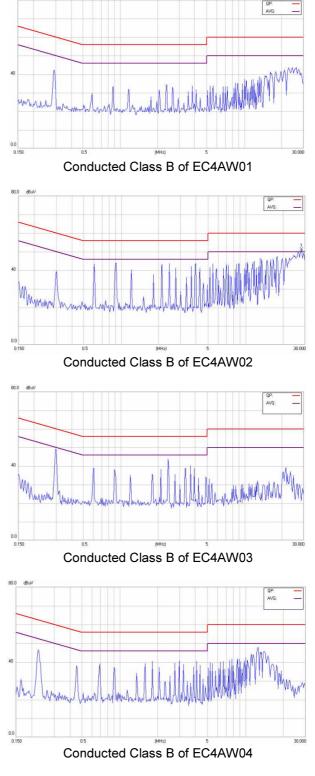
Note: C2 of capacitors are CHEMI-CON KY Series aluminum capacitors.

	EN55022 Class B			
Model No.	C1	C2	CY	L1
EC4AW01	47uF/100V	47uF/100V	NC	1uH
EC4AW02	47uF/100V	47uF/100V	NC	1uH
EC4AW03	NC	10uF/50V*5	NC	68uH
EC4AW04	47uF/100V	47uF/100V	NC	1uH
EC4AW05	47uF/100V	47uF/100V	NC	1uH
EC4AW06	47uF/100V	47uF/100V	NC	1uH
EC4AW07	47uF/100V	47uF/100V	NC	1uH
EC4AW11	47uF/100V	47uF/100V	NC	1uH
EC4AW12	47uF/100V	47uF/100V	NC	1uH
EC4AW13	47uF/100V	47uF/100V	NC	1uH
EC4A1W4	47uF/100V	47uF/100V	NC	1uH
EC4AW15	47uF/100V	47uF/100V	NC	1uH
EC4AW16	47uF/100V	47uF/100V	NC	1uH
EC4AW17	47uF/100V KY	47uF/100V KY	NC	1uH

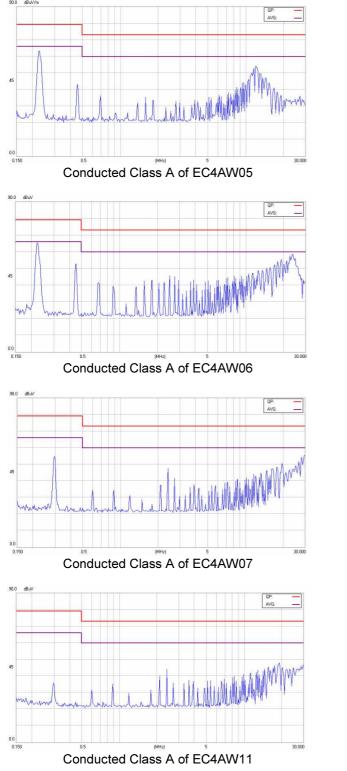
Note: C1, C2 of capacitors are CHEMI-CON KY Series aluminum capacitors, EC4AW03 C2 ceramic capacitor.

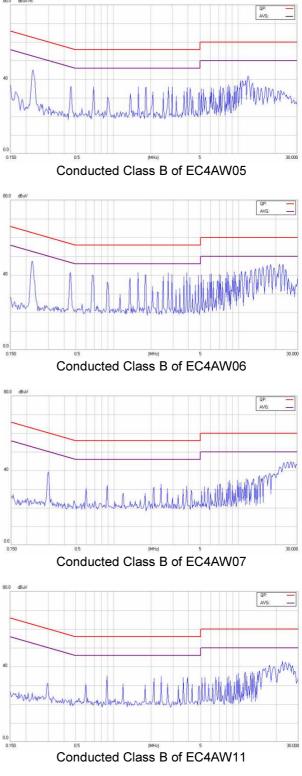




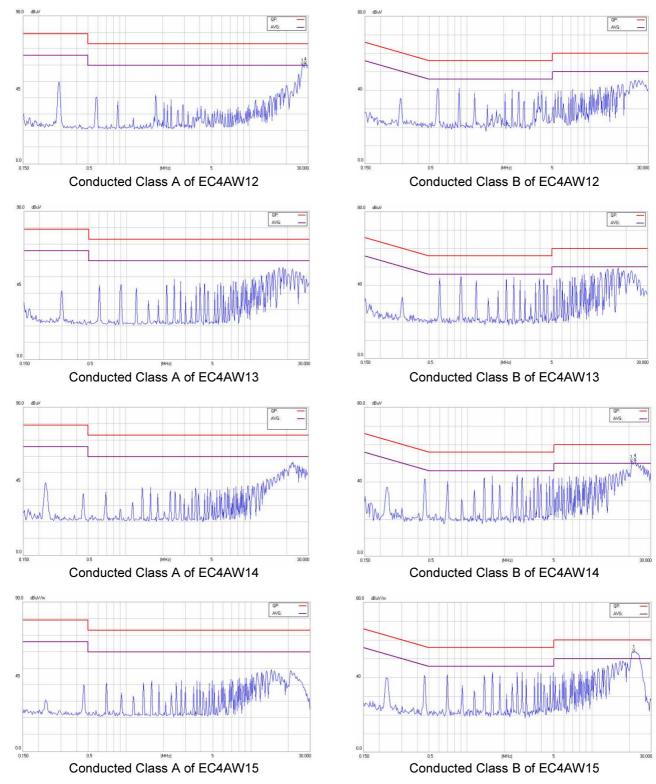




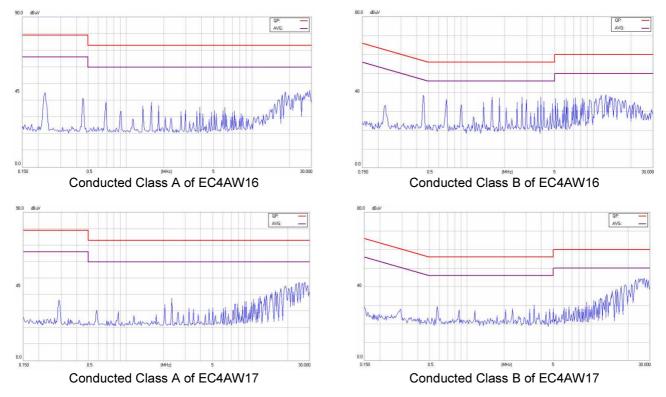








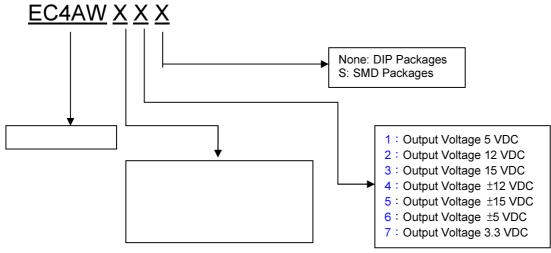




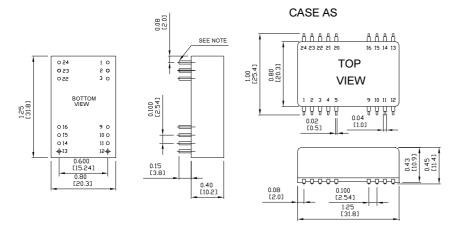


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8. Part Number



### 9. Mechanical Specifications



PIN CONNECTION					
Single Output		Dual Output			
DIP	SMD	DIP	SMD		
NP	NC	NP	NC		
-V I	nput	-V I	nput		
NP	NC	NP	NC		
NC		Common			
NC		NC			
N	NC		Dutput		
NP	NC	NP NC			
+V (	/ Output		Dutput		
-V (	-V Output		nmon		
NP	NC	NP NC			
+V I	+V Input +V Input				
	Single   DIP   NP   -VI   NP   NP   +V (   -V (   NP	Single Output   DIP SMD   NP NC   -V Input   NP NC   NC NC   NC V   NC V   NC V   NP NC   +V Output V   NP NC   +V Output NC   NP NC	Single Output Dual   DIP SMD DIP   NP NC NP   -V Input -V I   NP NC NP   NC NP NC   NC NC NP   NC -V I NP   NC NP NC   NP NC NP   +V Output +V C   -V Output Corr   NP NC NP		

\* NC-NO CONNECTION WITH PIN \* NP-NO PIN

#### Headquarter Office:

14F, No.306, Sec.4, Hsin Yi Rd., Taipei, Taiwan Tel: 886-2-27086210 Fax: 886-2-27029852 E-mail: <u>sales@cincon.com.tw</u> Web Site: http://www.cincon.com

#### Factory:

No. 8-1, Fu Kong Rd., Fu Hsing Industrial Park Fu Hsing Hsiang, ChangHua Hsien, Taiwan Tel: 886-4-7690261 Fax: 886-4-7698031

CINCON ELECTRONICS CO., LTD.

#### **Cincon American Office:**

1655 Mesa Verde Ave, Ste 180, Ventura, CA 93003 Tel: 805-639-3350 Fax: 805-639-4101 E-mail: info@cincon.com